

EXPRESS MAIL LABEL NO. EV314045464US

DATED: 03 FEBRUARY 2003

ATTORNEY DOCKET: MXIC 1564-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventor:

**Hsiang Lan Lung**

Application No. \_\_\_\_\_

Filed: 03 February 2004 (herewith)

Title: **Trap Read Only Non-Volatile  
Memory (TROM)**

Group Art Unit: unknown

Examiner: unknown

**CUSTOMER NO. 22470**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56**

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. The exception to this, in accordance with the 05 August 2003 Official Gazette Notice in which the USPTO waives the requirement for submitting copies of U.S. Patent and Publications for cases filed after 30 June 2003, is that copies of U.S. Patent documents and copies of U.S. Patent publications are not being submitted. Document C7 is also not enclosed because, although it is viewable on the listed website, it cannot be printed. The Examiner is requested to view the reference online.

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed within three months of the filing date of an application other than a continued prosecution application under 37 C.F.R. 1.53(d).

***Fee Authorization.*** The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (MXIC 1564-1). A copy of this authorization is enclosed.

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Date: 3 Feb 04

By: 

Mark A. Haynes, Reg. No. 30,846

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PTO/SB/08A (08-00)  
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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>		
Sheet		1	of	2	Application Number Filing Date First Named Inventor Group Art Unit Examiner Name Attorney Docket Number	
					3 February 2004 (herewith) Hsiang Lan Lung    MXIC 1564-1	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	A1	4,959,812		Momodomi et al.	09-25-1990	
	A2	5,270,969		Iwahashi	12-14-1993	
	A3	5,424,569		Prall	06-13-1995	
	A4	5,448,517		Tanaka	05-07-1996	
	A5	5,644,533		Lancaster et al.	07-01-1997	
	A6	5,768,192		Eitan	06-16-1998	
	A7	6,011,725		Eitan	01-04-2000	
	A8	6,172,907		Jenne	01-09-2001	
	A9	6,215,148		Eitan	04-10-2001	
	A10	6,436,768		Yang et al.	08-20-2002	
	A11	6,458,642		Yeh et al.	10-01-2002	
	A12	Re. 35,838		Momodomi et al.	07-07-1998	
	A13	2002/0179958		Kim	12-05-2002	
	A14					
	A15					
	A16					
	A17					
	A18					
	A19					
	A20					

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	B1							
	B2							
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	B6							
	B7							
	B8							
	B9							
	B10							

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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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		Application Number			
		Filing Date		3 February 2004 (herewith)	
		First Named Inventor		Hsiang Lan Lung	
		Group Art Unit			
		Examiner Name			
Sheet		2		of 2	
		Attorney Docket Number		MXIC 1564-1	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	C1	DE BLAUWE, JAN, "Nanocrystal Nonvolatile Memory Devices," IEEE Transactions on Nanotechnology, Vol. 1, No. 1 (March 2002) 72-77	
	C2	LEE, JAE-DUK, "Effects of Floating-Gate Interference on NAND Flash Memory Cell Operation," IEEE Electron Device Letters, Vol. 23, No. 5 (May 2002), 264-266	
	C3	LEE, CHANGHYUN, et al., "A Novel Structure of SiO <sub>2</sub> /SiN/High k Dielectrics, Al <sub>2</sub> O <sub>3</sub> for SONOS Type Flash Memory," Extended Abstracts of the 2002 International Conference on Solid State Device and Materials, Nagoya (2002), 162-163.	
	C4	SHIN, YOOCHEOL, et al., "High Reliable SONOS-Type NAND Flash Memory Cell with Al <sub>2</sub> O <sub>3</sub> for Top Oxide," Non-Volatile Semiconductor Memory Workshop (2003) 2 pages	
	C5	LEE, CHANG HYUN, et al., "A Novel SONOS Structure of SiO <sub>2</sub> /SiN/Al <sub>2</sub> O <sub>3</sub> with TaN Metal Gate for Multi-Giga Bit Flash Memories," IEEE (2003) 4 pages.	
	C6	CHUNG, STEVE S., et al., "A Novel Leakage Current Separation Technique in a Direct Tunneling Regime Gate Oxide SONOS Memory Cell," IEEE (2003), 4 pages.	
	C7	HIROSE, M., "Challenges for Future Semiconductor Development," Advance Semiconductor Research Center, National Institute for Advanced Industrial Science and Technology, International Microprocess and Nanotechnology Conference, (6 Nov. 2002), available at <a href="http://www.miraipj.jp/en/opendata/pdf/021106MNC.pdf">http://www.miraipj.jp/en/opendata/pdf/021106MNC.pdf</a> . NO COPY OF DOCUMENT PROVIDED, WEBSITE HAS DOCUMENT LOCKED, CAN ONLY VIEW ON INTERNET	

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